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7590

12/02/2005

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02/21/2006 TBESHAH2 00000040 502257 10620147

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Sandra L. Gao	(Depositor's name)
Sandra L. Gao	(Signature)
2-17-06	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/620,147	07/14/2003	Gilles Bosco	M-15228 US	8494

TITLE OF INVENTION: ALGORITHM TO INCREASE LOGIC INPUT WIDTH BY CASCADING PRODUCT TERMS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	03/02/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
BOWERS, BRANDON	2825	716-007000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☒ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,

(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1. Jonathan W. Hallman  
MacPherson Kwok
2. Chen & Heid
- 3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE **Lattice Semiconductor Corporation** (B) RESIDENCE: (CITY and STATE OR COUNTRY) **Hillsboro, Oregon**

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

- ☒ Issue Fee  
☒ Publication Fee (No small entity discount permitted)  
☐ Advance Order - # of Copies \_\_\_\_\_

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- ☐ A check in the amount of the fee(s) is enclosed.  
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☒ The Director is hereby authorized by charge the required fee(s), or credit any overpayment, to Deposit Account Number 50-2252 (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date 2-17-06

Typed or printed name Jonathan W. Hallman

Registration No. 42,1022

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Gilles Bosco  
Assignee: Lattice Semiconductor Corporation  
Title: Algorithm To Increase Logic Input Width By Cascading Product Terms  
Serial No.: 10/620,147 Filing Date: July 14, 2003  
Examiner: Brandon Bowers Group Art Unit: 2825  
Docket No.: M-15228 US Confirmation No. 8494

Irvine, California  
February 16, 2006

Fax No.: 1-571-273-2885

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- 1) Part B – Fee(s) Transmittal
- 2) “Fee Address” Indication Form
- 3) Change of Correspondence Address

Dated: 2-17-06

By: Saundra L. Carr  
Saundra L. Carr

Total Pages Being Transmitted: 4

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